

IN THE CLAIMS:

Please amend claims 1-2 and 5-6 as follows:

1. (Currently Amended) A compiler device to generate a code for performing a procedure call in a program having a plurality of threads including a master thread and a non-master thread, making use of an interface area that is dynamically allocated for ~~each~~ said non-master thread, when ~~a~~ said plurality of threads are processed in parallel.

cl 2. (Currently Amended) A compiler device comprising:
a plurality of threads having a master thread and a non-master thread;
code generating means for generating a code for determining a leading address of an interface area that is dynamically allocated for ~~each~~ said non-master thread, when at least one of said plurality of threads is processed; and

code converting means for converting a reference to the interface area in a source program into a code for referencing the interface area making use of the leading address determined by executing said code generated by said code generating means.

3. (Previously Presented) A compiler device of claim 2,
wherein said code generating means is constituted to generate a code for
calling a library for determining the leading address of the interface area that is dynamically
allocated for each thread.

4. (Previously Presented) A compiler device of claim 2,
wherein said code generating means is constituted to generate a code for
determining the leading address of the interface area designated by a user.

5. (Currently Amended) A computer-readable recording medium recorded
with a compiler program for causing a computer to realize a function to generate a code for
performing a procedure call in a program having a plurality of threads including a master
thread and a non-master thread, making use of an interface area that is dynamically allocated
for each said non-master thread, when a said plurality of threads are processed in parallel.

6. (Currently Amended) A computer-readable recording medium recorded
with a compiler program having a plurality of threads including a master thread and a non-
master thread, for causing a computer to realize:

a code generating function for generating a code for determining a leading address of an interface area that is dynamically allocated for each the non-master thread; when one of the plurality of threads is processed; and

a code converting function for converting: a reference to the interface area in a source program into a code for referencing the interface area making use of the leading address determined by executing said code generated by said code generating function.

C. 7. (Previously Presented) A computer-readable recording medium recorded with a compiler program of claim 6,

wherein said code generating function is constituted to generate a code for calling a library for determining the leading address of the interface area that is dynamically allocated for each thread.

8. (Previously Presented) A computer-readable recording medium recorded with a compiler program of claim 6,

wherein said code generating function is constituted to generate a code for determining the leading address of the interface area designated by a user.
